_
_
Q
<u></u>
_
Ø
w
_
_
N
Ν
0
0
_
Ω
_
\supset
_
Q
-
3
->
-
`
≷
-
>
S
5
$\overline{}$
$\overline{}$
- 1
0
O
_
-
_
_

Title Low level programming	Code 10103242910103201403
Field Flootrical Engineering	Year / Semester
Electrical Engineering	5/9
Specialty	Course
•	core
Hours	Number of credits
Lectures: - Classes: - Laboratory: 9 Projects / seminars: -	2
	Language
	polish

Lecturer:

Ph. D. Piotr Sujka tel. +48 61 6652662

e-mail: piotr.sujka@put.poznan.pl

Faculty:

Faculty of Electrical Engineering

ul. Piotrowo 3A 60-965 Poznań

tel. (061) 665-2539, fax. (061) 665-2548 e-mail: office_deef@put.poznan.pl

Status of the course in the study program:

Obligatory subject, Faculty of Electrical Engineering, Field: Electrical Engineering, Specialty: Mechatronic Electric Systems, Extramural first-degree studies

Assumptions and objectives of the course:

The student should obtain knowledge of the low level programming of processors (microcontrollers) in assembler

Contents of the course (course description):

Low level programming of processors (microcontrollers) in assembler

Introductory courses and the required pre-knowledge:

Knowledge of the structure and operating principle of selected microcontrollers. Processor registers and commands. Applications for programming selected microcontrollers

Courses form and teaching methods:

Laboratory

Form and terms of complete the course - requirements and assessment methods:

Valuation is based on the control of the knowledge and reports

Basic Bibliography:

Additional Bibliography: